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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application Number	To Be Assigned	
			Filing Date	Concurrently Herewith	
			First Named Inventor	Mario Au	
			Group Art Unit		
			Examiner Name		
Sheet	1	of	1	Attorney Docket Number	5646-124

U.S. PATENTS AND PATENT PUBLICATIONS					
Examiner Initials*	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Number	Kind Code (if known)		
		US-			
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FOREIGN PATENT DOCUMENTS							
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OTHER NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
GP	1	AVERLOGIC, AL4CS205, AL4CS215, AL4CS225, AL4CS235, AL4CS245 Data Sheets, Version 2.1, February 20, 2003, 39 pages	
GP	2	Cypress Semiconductor Corporation, Designing with CY7C436xx Synchronous FIFOs, August 4, 2000, 10 pages	

Examiner Signature	/Gary Portka/	Date Considered	06/15/2006
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

